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# Service Manua

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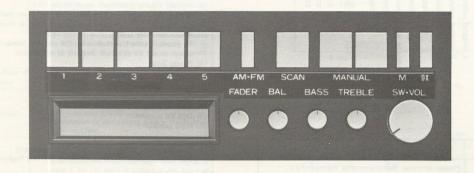
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## **NISSAN Automobile Genuine Diversity Radio** Model RN-9030M



## SPECIFICATIONS:

Circuit system: Tuning system: Superheterodyne Electronic tuning

Receive range:

AM 530 to 1,620kHz

FM 87.9 to 107.9MHz

Intermediate frequency:

AM 450kHz

FM 10.7MHz

Quieting sensitivity: AM Less than 33dB

(at 20dB S/N)

FM Less than 18dB

(at 30dB S/N)

Selectivity:

AM More than 50dB

(at + 10kHz detune)

Separation:

FM More than 15dB

(at 1kHz)

Fidelity:

AM 100Hz  $0 \pm 3dB$ 

(Bass, treble=flat)

1kHz OdB

 $4kHz - 14 \pm 5dB$ 

 $0 \pm 3dB$ FM 70Hz

1kHz OdB

 $7kHz - 18 \pm 5dB$ 

Auto tuning stop sensitivity:

AM DX 32±6dB

LO 52 ± 8dB

FM DX 32±6dB

LO 52 ± 8dB

Load impedance:

 $4\Omega \times 4$ 

Power output:

3.0W × 4

(at 3% distortion)

More than 5.0W×4

(at max. output)

Power supply voltage:

DC 13.2V (10.8V to 15.6V)

Negative ground

Current consumption:

Less than 5A

(at max. output)

Dimensions:

Weight:

Width 160mm

52mm Height

Depth 130mm

830g

## COMPONENT:

RN-9030M-A

Main unit

1

## **EXPLANATION OF IC's:**

## ■MPX-NC-V 051-0633-00 MPX with FM noise canceller

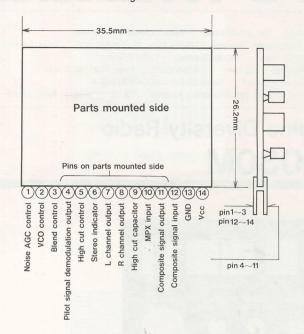
### Absolute Maximum Ratings

Power voltage Allowable power dissipation Operating temperature Storage temperature

Vcc max (Ta=25°C) P<sub>D</sub> max (Ta=25°C) Topa

16V 920mW -20~70°C -40~80°C

## **Terminal Connection Drawing**



| No. | Name of terminal                 | Description  |
|-----|----------------------------------|--|
| 14  | Vcc                              | Power terminal. Recommended Voltage; 12V.  |
| 13  | GND                              | Ground terminal.   |
| 12  | Composite signal input           | Composite signal demodulated by FM demodulator is input.   |
| 11  | Composite signal output          | Output from this terminal is the composite signal, the pulse type noise of which has been suppressed by the noise canceller.   |
| 10  | MPX In                           | Input terminal of MPX circuit.   |
| 9   | High cut capacitor               | Capacitor used for attenuating high frequency component of composite signal is connected.  |
| 8   | R channel output                 | Audio signal output of R channel.  |
| 7   | L channel output                 | Audio signal output of L channel.  |
| 6   | Stereo indicator                 | Stereo indicator LED is connected.   |
| 5   | High cut control                 | When voltage, less than 1.2V is applied to this terminal, the high frequency component of composite signal is attenuated.  |
| 4   | Pilot signal demodulation output | When this terminal is grounded, the audio output becomes monaural. For the stereo output, open this terminal.  |
| 3   | Blend control                    | When this terminal is set to OV, both L and R audic signals are mixed, thus producing the monaural mode. When a voltage of more than 1.8V is applied, the stereo output with the maximum separation is produced. |
| 2   | V.C.O. control                   | When voltage of more than 7V is applied to this terminal, V.C.O. for PLL MPX stops oscillating.  |
| 1   | Noise A.G.C. control             | When this terminal is set to "H", the gain of noise A.G.C. is set to a level suitable for the weak electric current field.   |

#### **■**μPD1708G-017 051-0713-00 PLL Frequency Synthesizer & Controller

#### Outline

### § 1 Receive Frequency, Channel Space, Reference Frequency, Intermediate Frequency

#### FM Band

| Frequency Range   | Channel Space | Reference Frequency | Intermediate Frequency |
|-------------------|---------------|---------------------|------------------------|
| 87.9~107.9MHz     | 200kHz        | 25kHz               | 10.7MHz                |
| 87.5~107.9MHz     | 100kHz        | 25kHz               | 10.7MHz                |
| 76.0~ 90.0MHz     | 100kHz        | 25kHz               | -10.7MHz               |
| 87.604~107.900MHz | 86kHz (Note)  | 5kHz                | -10.7MHz               |

Note: The frequency closest to the normal channel plan can be attained by stepping up or down the intended frequency by 80kHz or 90kHz each (maximum error±4kHz). Blanks are displayed for the 10kHz digit is expresed by 0 to 4 or "5" is displayed for the digits from 5 to 9 while none is displayed for the 1kHz digit.

### AM Band

| Frequency Range | Channel Space | Reference Frequency | Intermediate Frequency |
|-----------------|---------------|---------------------|------------------------|
| 530~1,620kHz    | 10kHz         | 10kHz               | 450kHz                 |
| 530~1,620kHz    | 5kHz          | 5kHz                | 450kHz                 |
| 522 ~ 1,629kHz  | 9kHz          | 9kHz                | 450kHz                 |
| 531 – 1,602kHz  | 9kHz          | 9kHz                | 450kHz                 |

§ 2 Channel Selection (1) Automatic Tuning (sawtooth wave mode)

(2) Manual-up/down ------Stepped shift by push-switch or continuous shift if depressed for 0.5sec or more and released.

(3) Preset Memory Call
To memorize the last station for FM and AM bands each by six button switches, which select six different stations of each band. (4) Preset Scan

To receive the preset channels for 5 seconds each out of the station.

## § 3 Tape Function

- (1) Tape run indication
- (2) METAL control
- (3) DOLBY control
- (4) MSS control

### § 4 Clock Function

(1) 12-hour clock display (with AM/PM indicators)
The colon display is selectable either for 1Hz flashing or continuous illumination

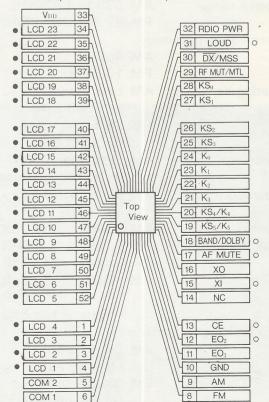
(1) LOUDNESS control

## II Absolute Maximum Ratings

Power Voltage  $V_{DD}$  max  $I_0$  max Output Current -10mA

## III Terminal Construction

Output terminal O ···Input terminal



| No.                           | Symbol  | Terminal<br>Name  | Function  |  |  |  |  |
|-------------------------------|---|---|---|--|--|--|--|
| 1-4<br>34~52                  | LCD1<br>LCD23   | LCD<br>Segment<br>Symbol                                    | LCD segment signal output terminals (each LCD has the 1/2 duty 1/2 bias, frame frequency 100Hz, drive voltage V <sub>DD</sub> )   |  |  |  |  |
| 5                             | COM2  | LCD Common  |   |  |  |  |  |
| 7 V <sub>DD</sub> Power Input |   | Caractus adi  | Device power terminals that supply $5V\pm 10\%$ during the device operation. The power can be selectively supplied to No. 7 or No. 33 terminal. The $V_{\rm DP}$ firse time should be 500ms or less $(0\rightarrow 4.5V)$ . If $V_{\rm DP}$ failed to drop fully to 0V because of an extremely long rise time and further it rose to 4.5V from below the operating voltage, the status of initialization diode switch may not be read correctly. In such a case use the CE terminal to read the correct status of the diode switch.   |  |  |  |  |
| 8                             | FM  | FM VCO Input  | Input terminal to receive FM local oscillator output. DC power must be cut out by the capacitor to protect the built-in AC amplifier.   |  |  |  |  |
| 9                             | AM  | AM VCO Input  | Input terminal to receive AM local oscillator output. DC power must be cut out by the capacitor to protect the built-in AC amplifier.   |  |  |  |  |
| 10                            | GND   | Ground  | Connect this terminal to the ground of the set.   |  |  |  |  |
| 11 12                         | EO <sub>1</sub> EO <sub>2</sub>                                 | Error Out<br>Output   | Charge pump outputs of phase detector composin PLL. If any part divided from the oscillation frequence exceeds the reference frequency, the high-level signal are output from both terminals or the low-level signal in the reverse case or the floating starts when bot coincide with each other.  Since the same signals are output to both terminals a a time, it is allowed to connect both to LPFs (low-pas filters) of AM or FM.  Device select signal input terminal; it must be set the high level to drive the PLL or to the low level to stop it. Either the clock or the blank display can be  |  |  |  |  |
| 15                            | XI  | Crystal   | chosen by the diode matrix during the low-level operation.  |  |  |  |  |
| 16                            | XO  | Oscillators   | 4.5MHz crystal oscillators must be connected here.  |  |  |  |  |
| 17                            | AF MUTE   | Mute Output   | Muting signal output terminal, active high, to eras shock noise caused by the PLL unlocking or by th tape/radio switching (CMOS output).  More information on its timing is given in the AF Mut Output Timing Chart.  |  |  |  |  |
| 18                            | BAND/<br>DOLBY  | BAND & DOLBY Output Terminal                                | It can be used as the FM/AM band switchin signal output terminal; the high level signal i output if the FM band is chosen or the low-leve signal if the AM band is selected.  TAPE mode: It can be used as the DOLBY ON/OFF signal output terminal; the high-level signal is output if " \( \sum_{\text{in}} \sum_{\text{in}} \subseteq \text{in} \text{ for the low-level signal} \) is displayed on the LCD panel or the low-level signal if the display is out.  The low-level signal (AM band or DOLBY OFF) is output at the initial power input (V_DD rise).   |  |  |  |  |
| 19                            | KS <sub>5</sub> /K <sub>5</sub> KS <sub>1</sub> /K <sub>1</sub> | Key Return<br>Signal Source<br>& Key Return<br>Signal Input | These can be used as the key return signal sources t read in the status of initialization diode matrix upon th initial power input (Vpp rise) or when returned fror the back up condition (CE low to high) only. Otherwise these terminals function as the key retur signal input terminals for key matrix. Connect th pull-down resistor. (CMOS input/outpu  |  |  |  |  |
| 21                            | K <sub>3</sub>  | Key Return<br>Signal Input                                  | Key return signal input terminals for key matrix. Connect the pull-down resistor. (CMOS inpu  |  |  |  |  |
| 25<br>,<br>28                 | KS <sub>3</sub> KS <sub>0</sub>                                 | Key Return<br>Signal Sources                                | Key return signal output terminals for the key matrix. No diodes need be provided on the key source sid that prevent reverse current flow, for the sink current is reduced as much as possible for the convenience of configuration. (CMOS output)  |  |  |  |  |
| 29                            | RF MUTE<br>METAL  | RF MUTE &<br>METAL<br>Output<br>Terminal                    | RADIO mode: It can be used as the stop sensing control signs output terminal for automatic tuning. If any of the automatic tuning keys (SEEK DOWN SEEK UP, SCAN DOWN, SCAN UP) is depressed the high level is effected about 60ms later fror the AF-MUTE and it is kept during the search.  TAPE mode: It can be used as the METAL ON/OFF signs output terminal. The high-level signal is output while "MTL" if displayed on the LCD panel or the low-level signal if the display is out. The low-level signal is output upon the initial powe input (VDD rise). (CMOS output input (VDD rise).   |  |  |  |  |
| 30                            | DX/MSS  | LOCAL/DX &<br>MSS Output<br>Terminal                        | RADIO mode:  It can be used as the stop sensing control signs output terminal for automatic tuning.  The low-level signal is output with "DX" displaye on the LCD panel if it failed to find any statio within one-cycle search from the frequency current ly being received, or the high-level signal is output while "DX" is out.  TAPE mode:  It can be used as the MSS ON/OFF signal output terminal.  The high-level signal is output while "MSS" is displayed on the LCD panel or the low-level signs is output if "MSS" is out.  The low-level signal is output upon the initial powe input (V <sub>D</sub> o rise). (CMOS output (CMOS outp |  |  |  |  |

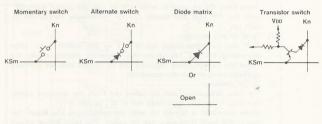
| Terminal<br>No. | Symbol         | Terminal<br>Name                      | Function  |
|-----------------|----------------|---------------------------------------|---|
| 31              | LOUDNESS       | LOUDNESS<br>Output<br>Terminal        | LOUDNESS signal output terminal; the high-level signal is output while "LOU" is displayed on the LCD pane or the low-level signal if "LOU" is out.  The low-level signal is output upon the initial powe input (V <sub>DD</sub> rise). (CMOS output             |
| 32              | RADIO<br>POWER | RADIO<br>ON/OFF<br>Output<br>Terminal | RADIO power ON/OFF signal output terminal wher<br>the momentary key method is chosen as the radio<br>power supply (with the diode switch RADSW=ON).<br>When RADIO is ON, the high-level signal is output or<br>the low-level if the switch is off. (CMOS output |

## IV Key Matrix § 1 Key Matrix Arrangement

| Input Terminals Output Terminals | K <sub>5</sub> (19) | K <sub>1</sub> (20) | K <sub>1</sub> (21) | K <sub>2</sub> (22) | K <sub>1</sub> (23) | K <sub>11</sub> (24) |
|----------------------------------|---------------------|---------------------|---------------------|---------------------|---------------------|----------------------|
| KS <sub>ii</sub> (28)            | SEEK DOWN           | SEEK UP             | PRESET<br>SCAN      | LOUDNESS            | OADJ                | ME                   |
| KS <sub>1</sub> (27)             | MD                  | MU                  | M4                  | M3<br>(DOLBY)       | M2<br>(METAL)       | M1<br>(MSS)          |
| KS <sub>2</sub> (26)             | SCAN<br>DOWN        | SCAN UP             | M6                  | M5                  | RCAL                | BAND                 |
| KS <sub>3</sub> (25)             |                     |                     | MODE                | SD (REW)            | STEREO (F/F)        | RADIO<br>ON/OFF      |
| KS <sub>1</sub> (20)             | PART SAR O          |                     | FMBND1              | FMBNDO              | AMBND1              | AMBNDO               |
| KS <sub>5</sub> (19)             |                     |                     | CELDSP              | PRIORITY            | RADSW               | CLKSEL               |

|   | Those in parentnesis denote the terminal numb        |
|---|--|
| : | Momentary switch                                     |
|   | Alternate or transistor switch                       |
|   | Diode matrix (to be shorted or kept open with diode) |
| : | Open   |
|   |  |

## § 2 Switch Connection



 $\S$  3 Initialization Diode Matrix Eight diode matrices are provided for initialization, and the status of each matrix is read in only upon the initial power input to  $V_{\rm DD}$  (Power-ON reset) or if the CE terminal level changes between low and high (CE reset); otherwise the status of diode matrices are ignored.

| Symbol   | Functions   |  |                           |  |  |  |  |
|----------|---|--|---------------------------|--|--|--|--|
|          | Both are the switches to set the receive FM band frequency range and the channel spaces as shown below.   |  |                           |  |  |  |  |
| FMBND1   | FMBND1  | FMBNDO   | Frequency Range           | Channel Space  |  |  |  |
|          | 0   | 0 0 87.9~107.9MHz  |                           | 200kHz   |  |  |  |
| FMBNDO   | 0   | 0 1 87.5~107.9MHz  |                           | 100kHz   |  |  |  |
|          | 1   | 0  | 76.0~ 90.0MHz             | 100kHz   |  |  |  |
|          | 1   | 1  | 87.604 ~ 107.900MHz       | 86kHz  |  |  |  |
|          | spaces as sh  | own below.   | ve AM band frequency ran  |  |  |  |  |
| AMBND1   | AMBND1  | AMBNDO   | Frequency Range           | Channel Space  |  |  |  |
| AMBNDO   | 0   | 0  | 530 ~ 1,620kHz            | 10kHz  |  |  |  |
|          | 0   | 1  | 530 ~ 1,620kHz            | 5kHz   |  |  |  |
|          | 1   | 0  | 522 ~ 1,629kHz            | 9kHz   |  |  |  |
|          | To-1 is   | 1  | 531 ~ 1,602kHz            | 9kHz   |  |  |  |
| RADSW    | method and<br>0·····Alte<br>1·····Mo  | Switch to select the radio power supply between the momentary key method and the alternate method.  OAlternate (CE terminal to be used)  1Momentary (CE terminal always set to high level and RADIO ON/OFF key to be used) |                           |  |  |  |  |
| CLKSEL   |   | t attached   | ock function is attached. | MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MACONE<br>MA |  |  |  |
| PRIORITY | Switch to select the priority display mode if the clock function is attached.  Either of the following two priority modes is displayed about 5sec. later when the other display mode is called.  OClock display  1Frequency display |  |                           |  |  |  |  |
| CELDSP   | Switch to select if clocks need be displayed when the clock function is attached and while RADIO is off.  0Blanks 1Clock display  |  |                           |  |  |  |  |

### § 4 Mode Switches

Unlike the initialization switches, the mode switches are always operable (in the table below "1" indicates the switch is turned ON or OFF if "0" is shown).

| Symbol          | Functions   |  |  |  |  |
|-----------------|---|--|--|--|--|
| MODE            | To select either the RADIO mode or the TAPE mode; 1TAPE mode 0RADIO mode  |  |  |  |  |
| SD (REW)        | RADIO mode:  It can be used as the station detector input under SEEK, SCAN and PRESET-SCAN. This must be turned off about 50ms after locking PLL.  SEEK, SCAN or PRESET-SCAN is automatically stopped as if the intended station has been detected if this switch has been found OFF by each of five detections that were made every 1ms.  TAPE mode:  It can be used as the tape direction display input switch.  "d" is displayed on the LCD panel when this switch is turned on. |  |  |  |  |
| STEREO<br>(F/F) | RADIO mode:  It can be used as the stereo signal input switch; when it is turned off, "ST" is displayed on the LCD panel but this display is kept out while the clocks are displayed or under the automatic tuning or PRESET-SCAN (the AF-MUTE terminal is active).  TAPE mode:  It can be used as the tape direction display input switch.  """ is displayed on the LCD panel when this switch is turned on.   |  |  |  |  |

## § 5 Momentary Switch

| Symbol   | Functions  |  |  |  |  |
|----------|--|--|--|--|--|
| MU<br>MD | Keys for the manual tuning or the clock adjustment.  o Frequency display  With each depression the frequency can be stepped up (MU) by one step (channel space) or down (MD). The frequency can be continuously stepped up or down by depressing the keys for 0.5sec. or more.  o Clock display  With the MU or MD key depressed together with the ME key, the minute digit or the hour digit can be adjusted.   |  |  |  |  |
|          | RADIO mode:  These keys can be used to write into, or call, the preset memory. Both the FM and AM bands can be individually memorized by each key.  (1) To write into memory With any key depressed out of M1 to M6 within 5sec. after depressing the ME key under the frequency display mode, the frequency current being received can be written into the memory corresponding to the depressed key.  (2) To call memory The data (frequency) can be called out from the memory corresponding to the key depressed out of M1 to M6. When the radio is turned on with the initial input V <sub>DD</sub> input, the lowest AM band frequency can be called. The table below shows the frequencies that are stored in the corresponding memories of M1 to M6 keys for the convenience of mass production of the sets. |  |  |  |  |

| Band | Preset Memory<br>Frequency Range | M1             | M2             | МЗ             | M4             | M5             | M6            |
|------|----------------------------------|----------------|----------------|----------------|----------------|----------------|---------------|
|      | 87.9 - 107.9MHz                  | 87.9           | 90.1           | 98.1           | 106.1          | 107.9          | 87.9          |
| FM   | 87.5 - 107.9MHz                  | 87.9           | 90.1           | 98.1           | 106.1          | 107.9          | 87.9          |
|      | 76.0 - 90.0MHz                   | 76.0           | 78.0           | 83.0           | 88.0           | 90.0           | 76.0          |
|      | 87.604 ~ 107.900MHz              | (Note)<br>87.6 | (Note)<br>89.3 | (Note)<br>93.6 | (Note)<br>97.9 | (Note)<br>99.6 | (Note<br>87.6 |
|      | 530 - 1,620kHz                   | 530            | 600            | 1,000          | 1,400          | 1,610          | 530           |
| AM   | 522 - 1,629kHz                   | 531            | 603            | 999            | 1,404          | 1,629          | 1,620         |
|      | 531 - 1,602kHz                   | 531            | 603            | 999            | 1,404          | 1,602          | 531           |

Note: The frequencies shown above are for display purpose only and differ from the actual frequencies that can be received.

TAPE mode:

M1 to M3 can be used to switch MSS, METAL and DOLBY; the other keys M4 to M6 are invalid.

OM1 (MSS)

M1 M6

Can be used to switch MSS.

With every depression both the MSS terminal output and the "MSS" display on the LCD panel are inverted.
With "MSS" kept on, the high-level signal is output from the MSS terminal or the low-level signal if "MSS" is out.

OM2 (METAL)

Can be used to switch METAL.

With every depression both the METAL terminal output and the 
"METAL" display on the LCD panel are inverted.

With "METAL" kept on, the high-level signal is output from the 
METAL terminal or the low-level signal if "METAL" is out.

OM3 (DOLBY)

OM3 (IOULBY)
Can be used to switch DOLBY.
With every depression both the DOLBY terminal output and the "DO" display on the LCD panel are inverted.
With "DOLBY" kept on, the high-level signal is output from the DOLBY terminal or the low-level signal if "DOLBY" is out.

| Symbol                  | Functions   |  |  |  |  |  |
|-------------------------|---|--|--|--|--|--|
| SEEK UP<br>SEEK<br>DOWN | Automatic tuning key to hold the current frequency even after the SD switch is turned off during the automatic tuning.  This mode can be further held on even if any of the LOUDNESS OADJ or ME keys is depressed, but the automatic tuning stops and it changes to the operation corresponding to any key that has been depressed.   |  |  |  |  |  |
| SCAN UP<br>SCAN<br>DOWN | Automatic tuning key to hold the current frequency for 5 second after the SD key is depressed during the automatic tuning; the sam frequency reception continues if either the SCAN UP or SCAN DOW! key is depressed during the above 5-sec. period. Or the automatic tuning suspended by the SD key is restored if no keys are depresse for the period of 5sec.  The automatic tuning continues even if the LOUDNESS, OADJ or M key is depressed, but the scanning stops if the ME key is depressed during the 5-sec. reception, and the set is changed to allow the memory write.  The automatic tuning stops with the depression of any other key and changes to the operation specified by the depressed key.   |  |  |  |  |  |
| ME                      | Preset memory write key or it can be used to adjust the clock if it is displayed. Frequency display mode:  This key can be used to write new frequencies into the prese memory. When it is depressed, "ME" is displayed on the LCD pane and it keeps on for 5 sec. even after this key is released. If any of M1 to M6 keys was depressed while "ME" was kept on, the displayed frequency is written into the memory corresponding to the depressed key.  To release the memory write mode, depress any key other than M1 to M6, ME and LOUDNESS. Clock display mode:  Both the "minutes" and "hours" digits can be adjusted by depressing either the MU or MD key together with this ME key.  If the MD key was depressed together with the ME key, the clock is advanced by one hour with each depression or it continues at a speed of 4 hours/sec, until the MD key is released after it has been depressed for 0.5 sec. or longer. This hour correction does not affect either the minute-digits or the second-digits (and the display remains out).  The clock advances by one minute with every depression of the MU key together with the ME key or it continues at a speed of 8 min/sec. until the MU key is released after it has been depressed for 0.5 sec. or longer. While they are not displayed, the seconds are reset by every minute correction to 0. No carry-over to the hour |  |  |  |  |  |
| OADJ                    | unit takes place, however.  This key can be used to set the clock on the hour. As it is depressed, the second counter insider is reset, and the clock is reset to Omin. Osec. by rounding off the minute digit even if it is less or more than 29. In the latter case the minute display is carried over to the hour digit or the clock is advanced by one hour.  |  |  |  |  |  |
| BAND                    | To switch the frequency band from FM to AM with each depression. The lowest AM frequency is called upon the initial power input ( $V_{\rm DB}$ rise).   |  |  |  |  |  |
| PRESET<br>SCAN          | Automatic preset scan key to start the scanning from the channel next to the preset channel being received (or from M1 if no preset channels being received) and receive the preset channel for 5 seconds that is selected when the SD switch is turned off. The same preset channel can be continued by the PRESET SCAN key before the above period of 5 seconds elapses.  The scanning continues even if the LOUDNESS, OADJ or ME key is depressed during the automatic preset scan mode, or the automatic preset scan stops if any other key is depressed, and it changes to the operation as specified by the depressed key.  |  |  |  |  |  |
|                         | To be used to switch LOUDNESS; the LOUDNESS output terminal and the "LOU" display on the LCD panel are inverted with every depression. The high-level signal is output from the LOUDNESS terminal while "LOU" is kept on or the low-level signal if the display is out. The "LOU" display is out upon the initial power input ( $V_{\rm DD}$ rise) and the low-level frequency is output from the LOUDNESS terminal.  |  |  |  |  |  |
| RCAL                    | Key to switch the display and valid under the RADIO mode only. With this key depressed, the clock display changes to the frequency display or vice versa, provided that the priority mode display is restored (according to PRIORITY of the diode matrix) 5sec. after this key is depressed.  This key is invalid if no clocks are provided (CLKSEL=0).   |  |  |  |  |  |

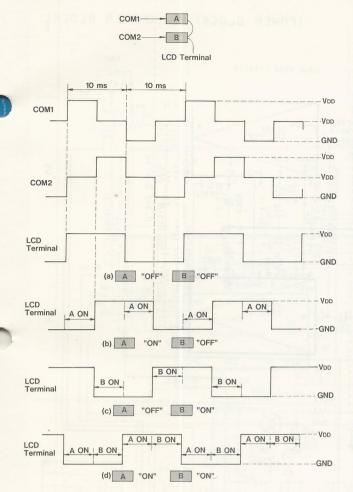
Note 1: The frequency is changed after confirming the PLL lock under the automatic tuning and PRESET SCAN modes. If, therefore, PLL is unlocked under either mode, the same frequency is held on until PLL is locked again. The momentary keys (other than RADIO OFF) cannot be controlled while the alternate switch is available.

To change the frequency during the set adjustment as PLL seems to be unlocked, use the memory buttons (M1~M6) or MU or MD to set the PLL

data without worrying about its unlocking.

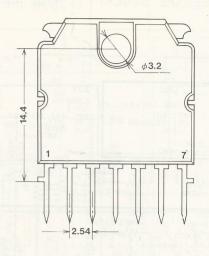
Note 2: If the frequency band is changed to the original band during the automatic tuning or PRESET SCAN mode, or if the power supply is resumed, the station finally being received is called out.

## LCD Drive Wave



## ■TA7274P 051-0735-00 Voice signal power amplifier (BTL 12W)

## Figure



Absolute Maximum Ratings (Ta=25°C)

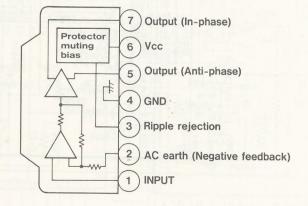
Operating supply voltage
Output current (Instantaneous value)
Allowable power dissipation Vcc opr lo (peak) 4.5A 15W

## **Electrical Characteristics**

(Unless otherwise specified, Vcc=13.2V, f=1kHz,  $R_L=4\Omega$ , Ta=25°C)

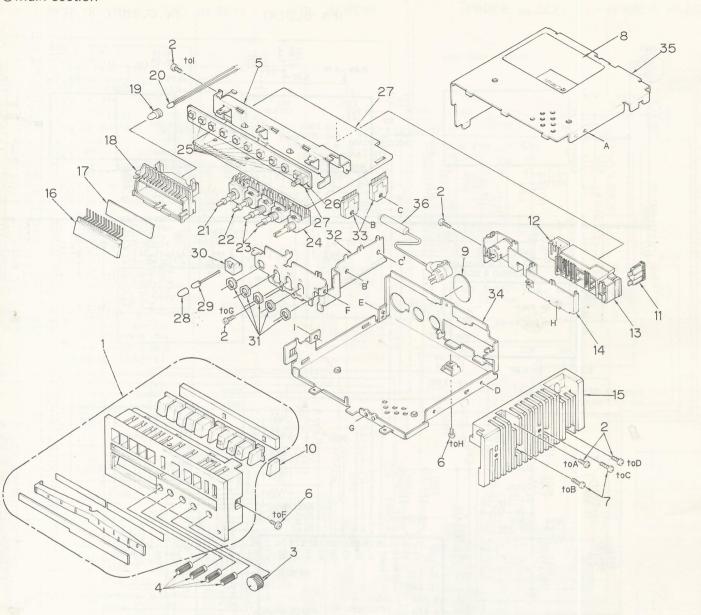
| Item                      | Symbol          | Measuring condition                        | Min. | Тур. | Max. | Unit   |  |
|---------------------------|-----------------|--|------|------|------|--------|--|
| Non-signal current        | Icco            | V <sub>IN</sub> =0                         | _    | 80   | _    | mA     |  |
| Output power              | Pour            | THD=10%                                    | 10   | 12   |      | W<br>% |  |
| Total harmonic distortion | THD             | P <sub>out</sub> =5W                       | 51.5 | 0.4  |      |        |  |
| Voltage gain              | Gv              | Rf=00                                      |      | 53.5 | 55.5 | dB     |  |
| Output noise voltage      | V <sub>NO</sub> | $Rg = 10k\Omega$ ,<br>BW = 20Hz - 20kHz    | _    | 0.9  | 2.0  | mVrms  |  |
| Ripple rejection          | R-R             | $f_{ripple} = 100Hz$ ,<br>$Rg = 600\Omega$ | _    | 50   | 1_5  | dB     |  |
| Input resistance          | R <sub>IN</sub> | f=1kHz                                     | _    | 30   | _    | kΩ     |  |
| Output offset voltage     | Voffset         | V <sub>IN</sub> =0                         |      | 0    | 0.3  | V      |  |

## Terminal Connection Diagram and Block Diagram



## EXPLODED VIEW · PARTS LIST:

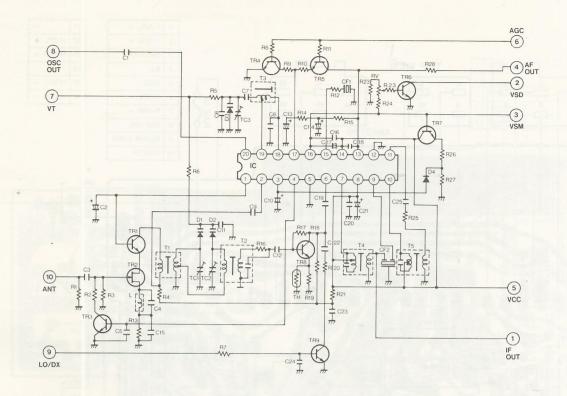
Main section



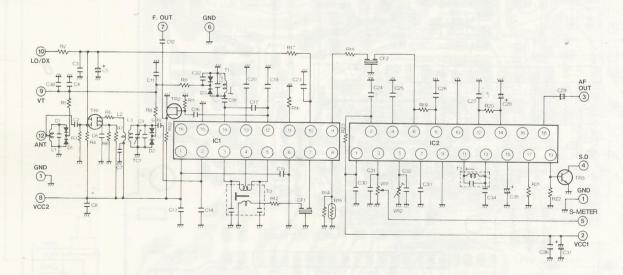
| REF.NO. | PART NO. (ORDER NO.) | DESCRIPTION                  | Q'TY |
|---------|----------------------|------------------------------|------|
| 1       | 940-0503A            | Escutcheon ass'y             | 1    |
| 2       | 731-3006-80          | Tap tight (M3x6)             | 5    |
| 3       | 380-4654-01          | Knob (POWER)                 | 1    |
| 4       | 380-4655-00          | Knob                         | 4    |
| 5       | 330-8235-01          | P.W.B holder                 | 1    |
| 6       | 714-3006-81          | Machine screw<br>(M3x6)      | 2    |
| 7       | 714-3010-81          | Machine screw<br>(M3x10)     | 2    |
| 8       | 286-5602-02          | Set plate                    | 1    |
| 9       | 290-2567-01          | Label                        | 1    |
| 10      | 380-4661-00          | Knob (DX/LOC)                | 1    |
| 11      | 060-0057-04          | Fuse (5A)                    | 1    |
| 12      | 074-0713-00          | Outlet socket<br>(DIN 5P)    | 1    |
| 13      | 074-0714-32          | Outlet socket<br>(S-10P, 6P) | 1    |
| 14      | 330-8209-01          | DIN holder                   | 1    |
| 15      | 313-1229-01          | Heat sink                    | 1    |
| 16      | 379-0092-00          | Indicator (LCD)              | 1    |
| 17      | 335-1993-00          | Reflector                    | 1    |
| 18      | 335-1992-00          | LCD holder                   | 1    |

| REF.NO. | PART NO. (ORDER NO.) | DESCRIPTION                      | Q'TY |
|---------|----------------------|----------------------------------|------|
| 19      | 345-3667-08          | P.L cap                          | 1    |
| 20      | 017-0367-00          | Pilot lamp                       | 1    |
| 21      | 012-4350-00          | Variable resistor<br>(FADER)     | 1    |
| 22      | 012-4352-00          | Variable resistor<br>(BAL)       | 1    |
| 23      | 012-4351-00          | Variable resistor<br>(BASS, TRE) | 2    |
| 24      | 012-4353-00          | Variable resistor<br>(POWER)     | 1    |
| 25      | 013-3776-00          | Switch                           | 10   |
| 26      | 013-3670-01          | Switch (DX/LOC)                  | 1    |
| 27      | 099-7427-00          | P.W.B                            | 1    |
| 28      | 345-2830-68          | P.L cap                          | 1    |
| 29      | 017-0345-00          | Pilot lamp                       | 1    |
| 30      | 345-4067-00          | P.L holder                       | 1    |
| 31      | 722-0282-00          | Nut (M7)                         | 5    |
| 32      | 330-8210-01          | IC holder                        | 1    |
| 33      | 051-0735-00          | Power IC (TA7274P)               | 2    |
| 34      | 311-1186-01          | Lower case                       | 1    |
| 35      | 310-1222-01          | Upper case                       | 1    |
| 36      | 093-0857-01          | Antenna jumper                   | 1    |

## ■AM TUNER PACK: 941-0141-00



## ■FM TUNER PACK: 941-0143-00



## **PARTS LIST:**

## ©Electrical section

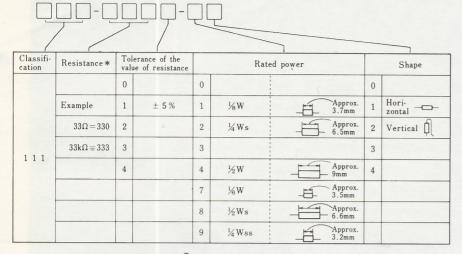
| REF.NO.   | PART NO. (ORDER NO.) | DESCRIPTION                       | Q'T |
|---|----------------------|-----------------------------------|-----|
| $D^{1,102,301}_{302,401,402}_{502-508,602}_{604,605,607}$ | 001-0330-00          | Diode (1SS119)                    | 17  |
| D <sub>303</sub>  | 001-0356-00          | Diode (1SS184)                    | 1   |
| D <sub>608~611</sub>                                      | 001-0360-00          | Diode (S5566B)                    | 4   |
| D <sub>103,104</sub>                                      | 001-0361-00          | Diode (1SS198)                    | 2   |
| D <sub>601,603,606</sub>                                  | 001-0424-19          | Diode (MA4056)                    | 3   |
| D <sub>2,3,501</sub>                                      | 001-0424-24          | Diode (MA4091)                    | 3   |
| L 601   | 009-0643-00          | Choke                             | 1   |
| L 103   | 010-2046-44          | Coil (1 <sub>µ</sub> H)           | 1   |
| L 101   | 010-2047-67          | Coil (5.6 <sub>\(\mu\)</sub> -10) | 1   |
| L 102   | 010-2047-86          | Coil (220 <sub>µ</sub> -10)       | 1   |
| L 201   | 010-2053-00          | Coil                              | 1   |
| RY <sub>601</sub>   | 014-0520-00          | Relay                             | 1   |
| PL <sub>201</sub>   | 017-0321-01          | Pilot lamp (NEON)                 | . 1 |
| CCT <sub>501</sub>  | 050-0093-03          | Component circuit                 | 1   |
| IC <sub>302</sub>   | 051-0606-00          | IC (BT3S501)                      | 1   |
| IC <sub>102</sub>   | 051-0633-00          | IC (NC-MPX)                       | 1   |
| IC <sub>501</sub>   | 051-0713-00          | IC (μPD1708G-017)                 | 1   |
| IC <sub>301,401</sub>                                     | 051-0735-00          | IC (TA7274P)                      | 2   |
| X <sub>501</sub>  | 061-1037-00          | Crystal (4.5MHz)                  | 1   |
| $Q_1$   | 100-0684-50          | Transistor<br>(2SA684RS)          | 1   |
| Q <sub>505,603</sub>                                      | 100-1175-60          | Transistor<br>(2SA1175)           | 2   |
| Q <sub>501</sub>  | 102-1545-02          | Transistor<br>(2SC1545B)          | 1   |
| Q102,103,104,301<br>401,502-504<br>601,602,604            | 102-2458-50          | Transistor<br>(2SC2458YGRBL)      | 11  |
| Q <sub>606</sub>  | 102-2712-07          | Transistor<br>(2SC2712G)          | 1   |
| Q <sub>3,5</sub>  | 103-1225-50          | Transistor<br>(2SD1225)           | 2   |
| Q <sub>302,402</sub>                                      | 103-1450-00          | Transistor<br>(2SD1450)           | 2   |
| Q <sub>506,507</sub>                                      | 125-0001-02          | Transistor<br>(UN2112)            | 2   |
| Q <sub>2,4,6</sub>  | 125-2003-02          | Transistor<br>(RN1202)            | 3   |
| Q <sub>101,508,605</sub>                                  | 125-2005-02          | Transistor                        | 3   |
| R <sub>311,411</sub>                                      | 032-0074-11          | Film resistor                     | 2   |
| R <sub>617</sub>  | 111-1001-81          | (5W0.47Ω)<br>Film resistor        | 1   |
| R <sub>1</sub>  | 111-2291-81          | (½Ws10Ω) Film resistor            | 1   |
| R <sub>616</sub>  | 111-3901-82          | (½Ws2.2Ω) Film resistor           | 1   |
| R <sub>3</sub>  | 111-4711-81          | (½Ws39Ω) Film resistor            | 1   |
| R <sub>103</sub>  | 116-1001-10          | (½Ws470Ω)<br>Chip resistor        | 1   |
| R 102,107,113,301<br>302,303,401,402<br>403,602,608       | 116-1031-10          | (10Ω) Chip resistor               | 11  |
| R <sub>115,315</sub>                                      | 116-1041-10          | (10kΩ) Chip resistor              | 2   |
| R <sub>603,615</sub> <sup>111,312,412</sup>               | 116-1231-10          | (100kΩ) Chip resistor             | 5   |
| R <sub>105,601</sub>                                      | 116-1531-10          | (12kΩ) Chip resistor              | 2   |
| R <sub>313</sub>  | 116-1821-10          | (15kΩ) Chip resistor              | 1   |
| R <sub>114,612,613,614</sub>                              | 116-2221-10          | (1.8kΩ) Chip resistor             | 4   |
| R <sub>514</sub>  | 116-1541-10          | (2.2kΩ) Chip resistor             | 1   |
|   |                      | (150k)<br>Chip resistor           |     |
| R <sub>106</sub>  | 116-2731-10          | (27kΩ)                            | 1   |

| REF.NO.                      | PART NO. (ORDER NO.) | DESCRIPTION                                    | Q'TY |
|------------------------------|----------------------|--|------|
| R <sub>112</sub>             | 116-4701-10          | Chip resistor (47Ω)                            | 1    |
| R <sub>408</sub>             | 116-4711-10          | Chip resistor (470Ω)                           | 1    |
| R <sub>116,314</sub>         | 116-4721-10          | Chip resistor (4.7kΩ)                          | 2    |
| R <sub>104,506,611</sub>     | 116-4731-10          | Chip resistor (47kΩ)                           | 3    |
| R <sub>607</sub>             | 116-5611-10          | Chip resistor (560Ω)                           | 1    |
| R <sub>616</sub>             | 116-5631-10          | Chip resistor (56kΩ)                           | 1    |
| R <sub>116,117</sub>         | 116-6821-10          | Chip resistor (6.8kΩ)                          | 2    |
| R <sub>110</sub>             | 116-8221-10          | Chip resistor (8.2kΩ)                          | 1    |
| C <sub>309,310,409,410</sub> | 043-0122-00          | Ceramic capacitor (12V0.3 <sub>µ</sub> F)      | 4    |
| C <sub>113,114</sub>         | 043-0165-20          | Ceramic capacitor (16V0.1µF)                   | 2    |
| C <sub>107</sub>             | 160-1522-05          | Ceramic capacitor<br>(1500pF)                  | 1    |
| C <sub>504,506,509</sub>     | 171-1033-06          | Ceramic capacitor (0.01 µF)                    | 3    |
| C <sub>502</sub>             | 171-2233-06          | Ceramic capacitor (0.022µF)                    | 1    |
| C <sub>101</sub>             | 174-1800-13          | Ceramic capacitor<br>(18pF CH)                 | 1    |
| C <sub>201,507,508</sub>     | 175-2202-00          | Ceramic chip capacitor<br>(22pF CH)            | 3    |
| C <sub>103,202,203</sub>     | 177-1032-05          | Ceramic chip capacitor (0.01 µF K)             | 3    |
| C <sub>306,406</sub>         | 177-1222-05          | Ceramic chip capacitor<br>(1200pF K)           | 2    |
| C <sub>105</sub>             | 177-2222-05          | Ceramic chip capacitor<br>(2200pF K)           | 1    |
| C <sub>115</sub>             | 177-3332-05          | Ceramic chip capacitor (0.033 µF K)            | 1    |
| C <sub>104</sub>             | 177-6812-05          | Ceramic chip capacitor<br>(680pF K)            | 1    |
| C <sub>106</sub>             | 177-6822-05          | Ceramic chip capacitor (0.0068µF)              | 1    |
| C <sub>110,111</sub>         | 177-6835-06          | Ceramic chip capacitor (0.068 µF F)            | 2    |
| C <sub>115</sub>             | 173-1041-10          | Polyester capacitor (0.068 µF U)               | 1    |
| C <sub>607~609</sub>         | 173-1042-10          | Polyester capacitor (50V0.1μF)                 | 3    |
| C <sub>311, 411, 415</sub>   | 173-1542-10          | Polyester capacitor (50V0.15µF K)              | 3    |
| C <sub>102</sub>             | 173-3332-10          | Polyester capacitor (50V0.033µF)               | 1    |
| C <sub>503</sub>             | 173-5632-10          | Polyester capacitor (0.056 µF)                 | 1    |
| C <sub>315</sub>             | 042-0348-00          | Electrolytic capacitor (16V2200µF)             | 1    |
| C <sub>312,412,405</sub>     | 179-1053-62          | Electrolytic capacitor (50V1 µF)               | 3    |
| C <sub>314,603,604</sub>     | 179-1063-32          | Electrolytic capacitor (16V10μF)               | 3    |
| C <sub>606</sub>             | 179-1073-33          | Electrolytic capacitor (16V100 <sub>µ</sub> F) | 1    |
| C <sub>501,610</sub>         | 179-2263-32          | Electrolytic capacitor (16V22μF)               | 2    |
| C <sub>308,408</sub>         | 179-2273-23          | Electrolytic capacitor (10V220 <sub>µ</sub> F) | 2    |
| C <sub>407</sub>             | 179-4753-62          | Electrolytic capacitor $(50V4.7\mu\text{F})$   | 1    |
| C <sub>1,2,109,313</sub>     | 179-4763-22          | Electrolytic capacitor $(10V47\mu F)$          | 4    |
| C 108,303,304,305            | 182-1053-62          | Electrolytic capacitor (50V1 µF)               | 6    |
| C <sub>204,505</sub>         | 182-1056-62          | Electrolytic capacitor (50V1µF NP)             | 2    |
| C <sub>302,402,605</sub>     | 182-1063-32          | Electrolytic capacitor (16V10μF)               | 3    |
| C <sub>602</sub>             | 182-1073-22          | Electrolytic capacitor (10V100μF)              | 1    |
| C <sub>307</sub>             | 182-4753-52          | Electrolytic capacitor (35V4.7μF)              | 1    |
| C <sub>301,401</sub>         | 182-4756-32          | Electrolytic capacitor (16V4.7µF NP)           | 2    |
| C <sub>112</sub>             | 182-4763-12          | Electrolytic capacitor                         | 1    |
| C <sub>601</sub>             | 182-4743-62          | (6.3V47μF) Electrolytic capacitor              | 1    |
|                              |                      | (50V0.47μF)                                    |      |

## How to read resistor

Resistors are deleted from the table of electric components, (except metal film resistors and special resistors). They can be converted to product Nos. as follows.

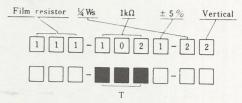
## Film resistor (Carbon film resistor)



| Exam   | Example of conversion of resistance Note) R: Resistance, T: Convert |          |     |         |     |          |     |           |     |          |     |           |     | verted    | valu |          |     |           |     |
|--|---|----------|-----|---------|-----|----------|-----|-----------|-----|----------|-----|-----------|-----|-----------|------|----------|-----|-----------|-----|
| R  | Т   | R        | T   | R       | Т   | R        | T   | R         | T   | R        | ·T  | R         | Т   | R         | Т    | R        | Т   | R         | Т   |
| $\begin{array}{c} \Omega \\ 0.1 \end{array}$ | 108   | Ω<br>1 0 | 109 | Ω<br>10 | 100 | Ω<br>100 | 101 | kΩ<br>1.0 | 102 | kΩ<br>10 | 103 | kΩ<br>100 | 104 | MΩ<br>1.0 | 105  | MΩ<br>10 | 106 | MΩ<br>100 | 107 |
| 0 15   | 158   | 1 5      | 159 | 15      | 150 | 150      | 151 | 1.5       | 152 | 15       | 153 | 150       | 154 | 1.5       | 155  | 15       | 156 | 150       | 157 |

| COLOR             | BLK | BRN | RED             | ORG | YEL | GRN | BLU             | PUR | GRY | WHT | GOLD           | SILVER      | NO<br>COLOR    |
|-------------------|-----|-----|-----------------|-----|-----|-----|-----------------|-----|-----|-----|----------------|-------------|----------------|
| 1st<br>color band | 0   | 1   | 2               | 3   | 4   | 5   | 6               | 7   | 8   | 9   |                |             |                |
| 2nd<br>color band | 0   | 1   | 2               | 3   | 4   | 5   | 6               | 7   | 8   | 9   |                |             |                |
| 3rd<br>color band | 10° | 10¹ | 10 <sup>2</sup> | 10³ | 104 | 105 | 10 <sup>6</sup> |     |     |     | 10-1           | 10-2        |                |
| 4th<br>color band |     |     |                 |     | 1   |     |                 |     |     |     | ± 5 %<br>( J ) | ±10%<br>(K) | ± 20%<br>( M ) |

## (Example)



Note 1. The first two of three digits representing resistance are effective digits and the last one represents number of "0" following this.

Unit is given in ohm  $(\Omega)$ .

