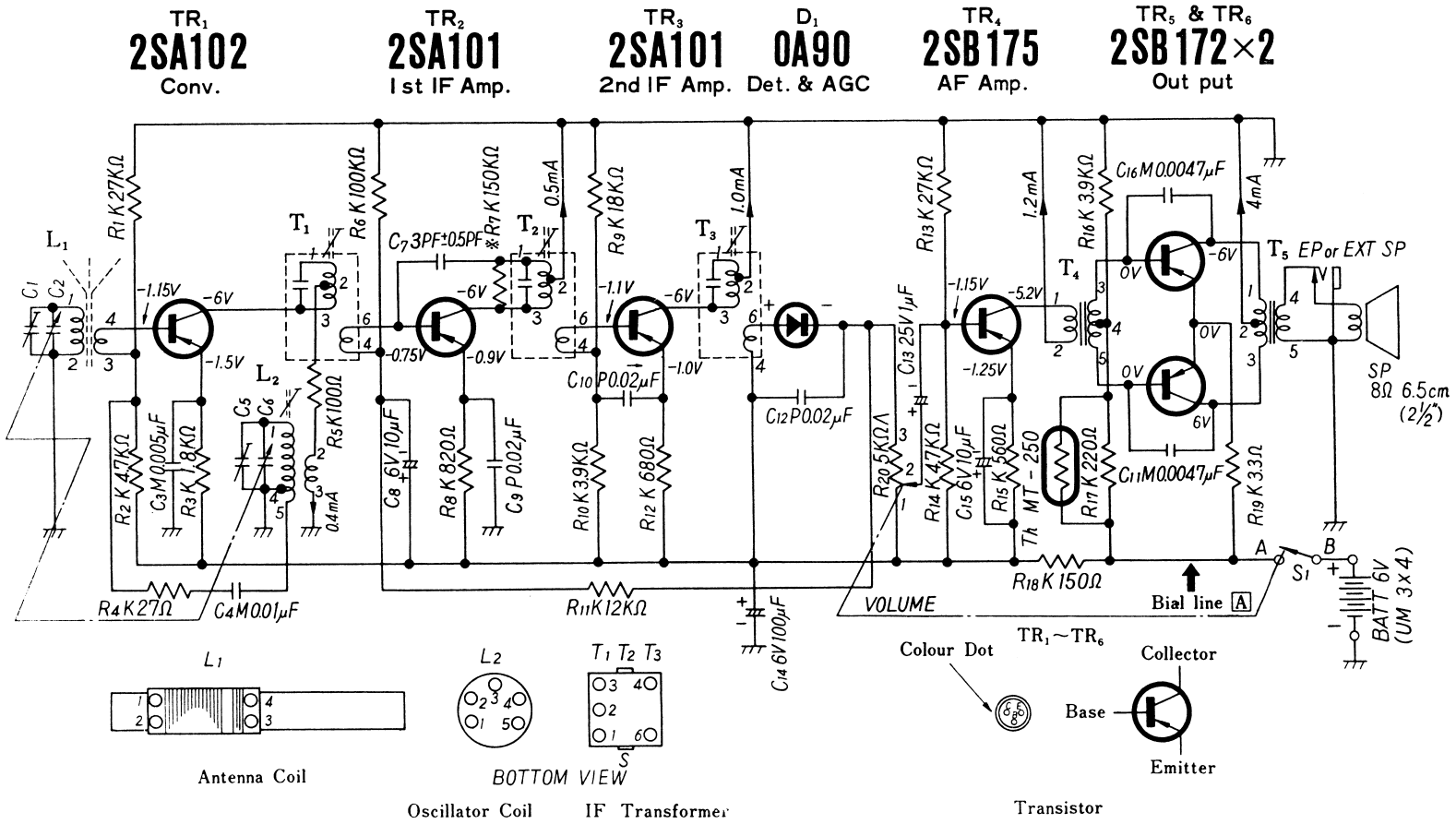
NATIONAL MODEL R-122



Notes:

1. S_1 : Power source switch in "OFF" positon.
2. DC Voltage measurement are taken with circuit tester (10KΩ/V).
3. Measured voltages for $TR_{1\sim6}$ are from transistor terminal to bias line [A].
4. Capital letters (M, K, J, P) in the circuit diagram show allowable tolerance of resistors and capacitors as follows.
   M=±20% K=±10% J=±5% P=±100%
5. Battery current: No signal 6~12mA
   Maximum output 100mA
6. PF = pico farad = mmf
   μF = micro fard = mfd
7. The resistor dotted in the diagram are the standard valuse which may be variable according to the characteristics of transistor.
   * R_7 = 220KΩ, 150KΩ

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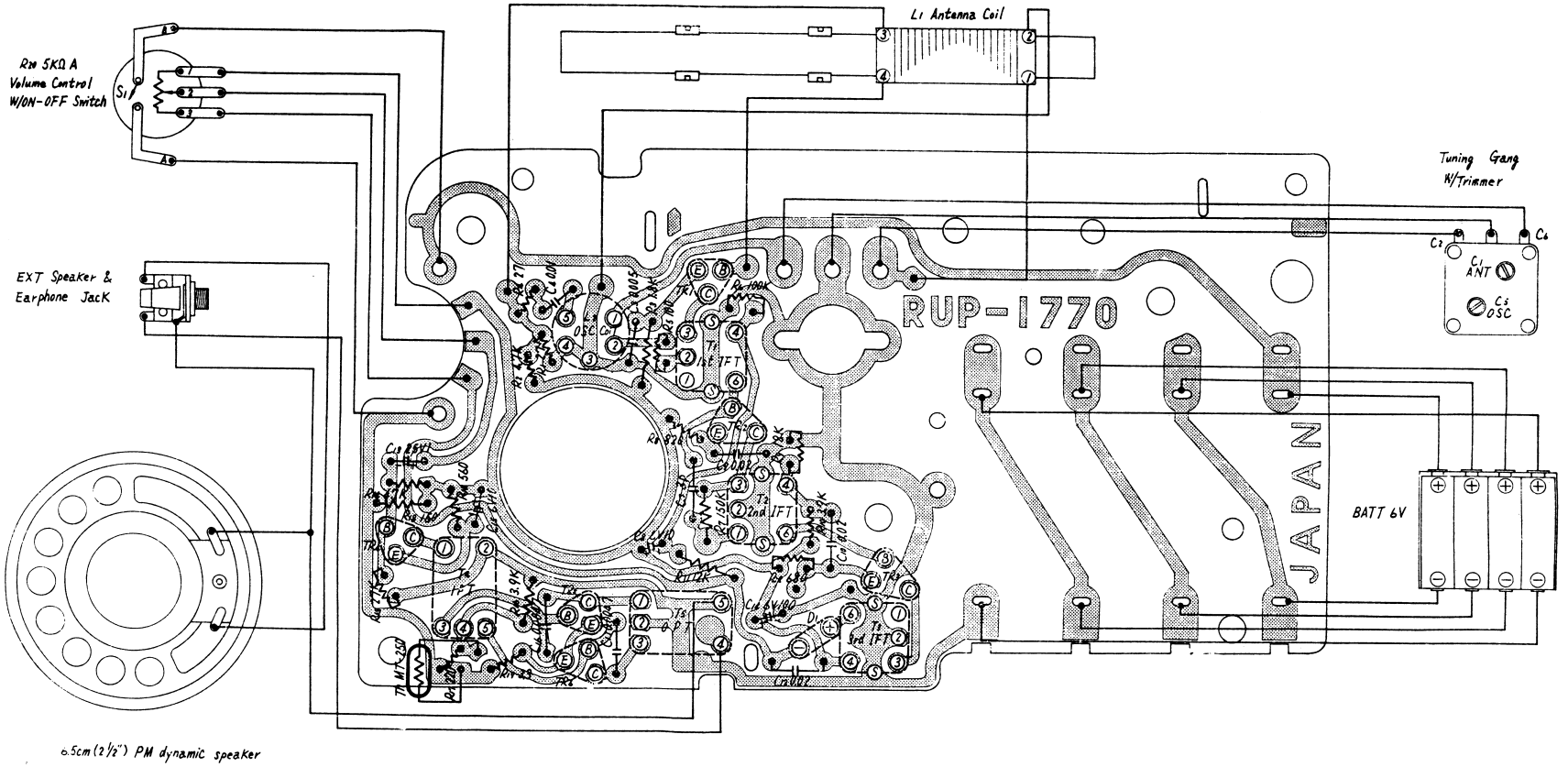

Notes:

1. All resistor values in ohms. (K = 1000Ω)
2. All capacitor values in micro farads. (P=μμF)
3. S_1 : Power source switch in "OFF" position.

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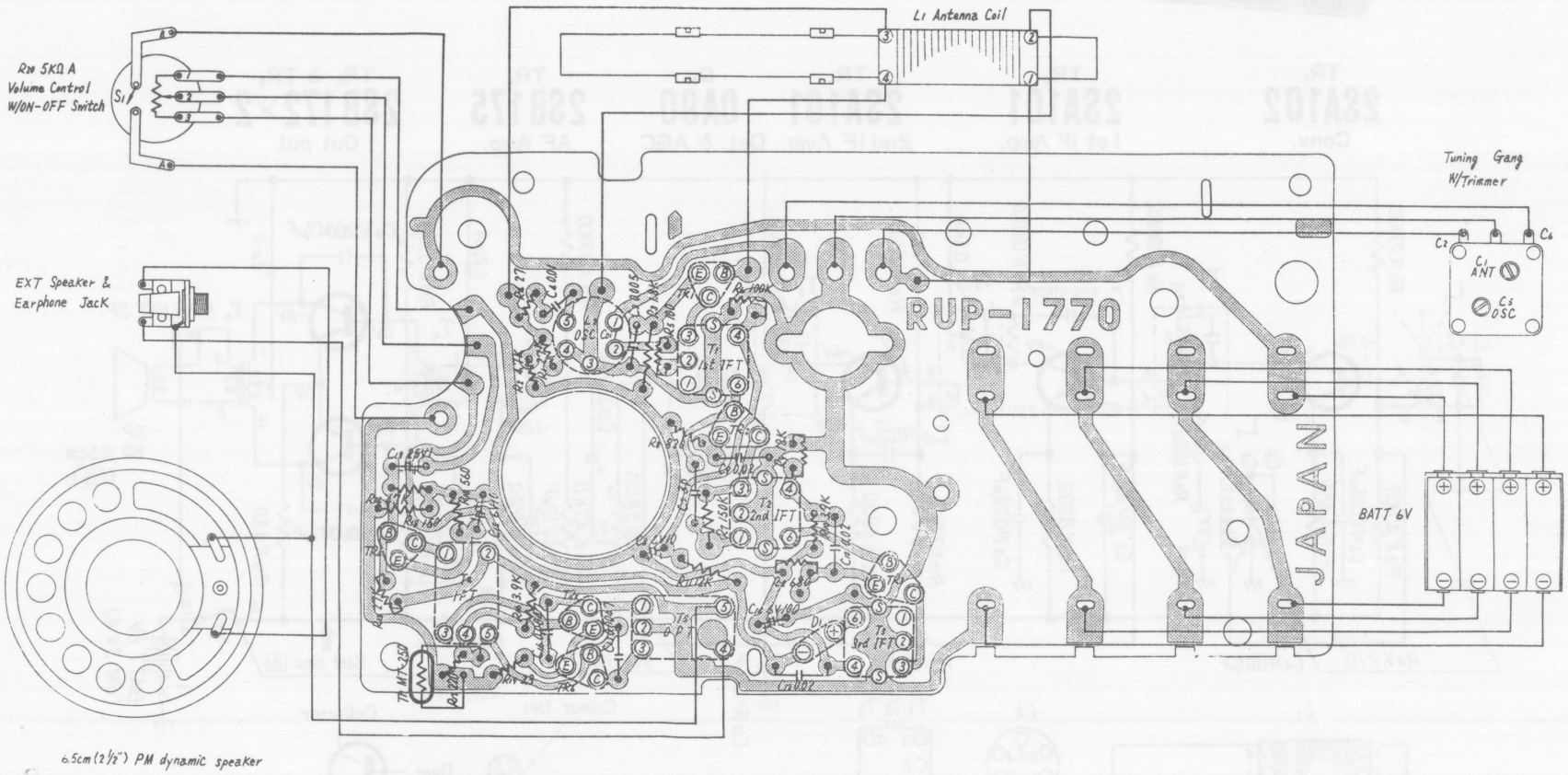

Notes:

1. All resistor values in ohms. (K = 1000Ω)
2. All capacitor values in micro farads. (P=μμF)
3. S_1 : Power source switch in "OFF" position.