**Notes:**

1. S<sub>1</sub>: Power source switch in "OFF" position.
2. DC Voltages measurements are taken with circuit tester (10K/10V).
3. Measured voltages for TR<sub>1</sub>~TR<sub>7</sub> are from transistor terminal to positive side of battery.
4. Capital letters (M, K, J, P) in the circuit diagram show

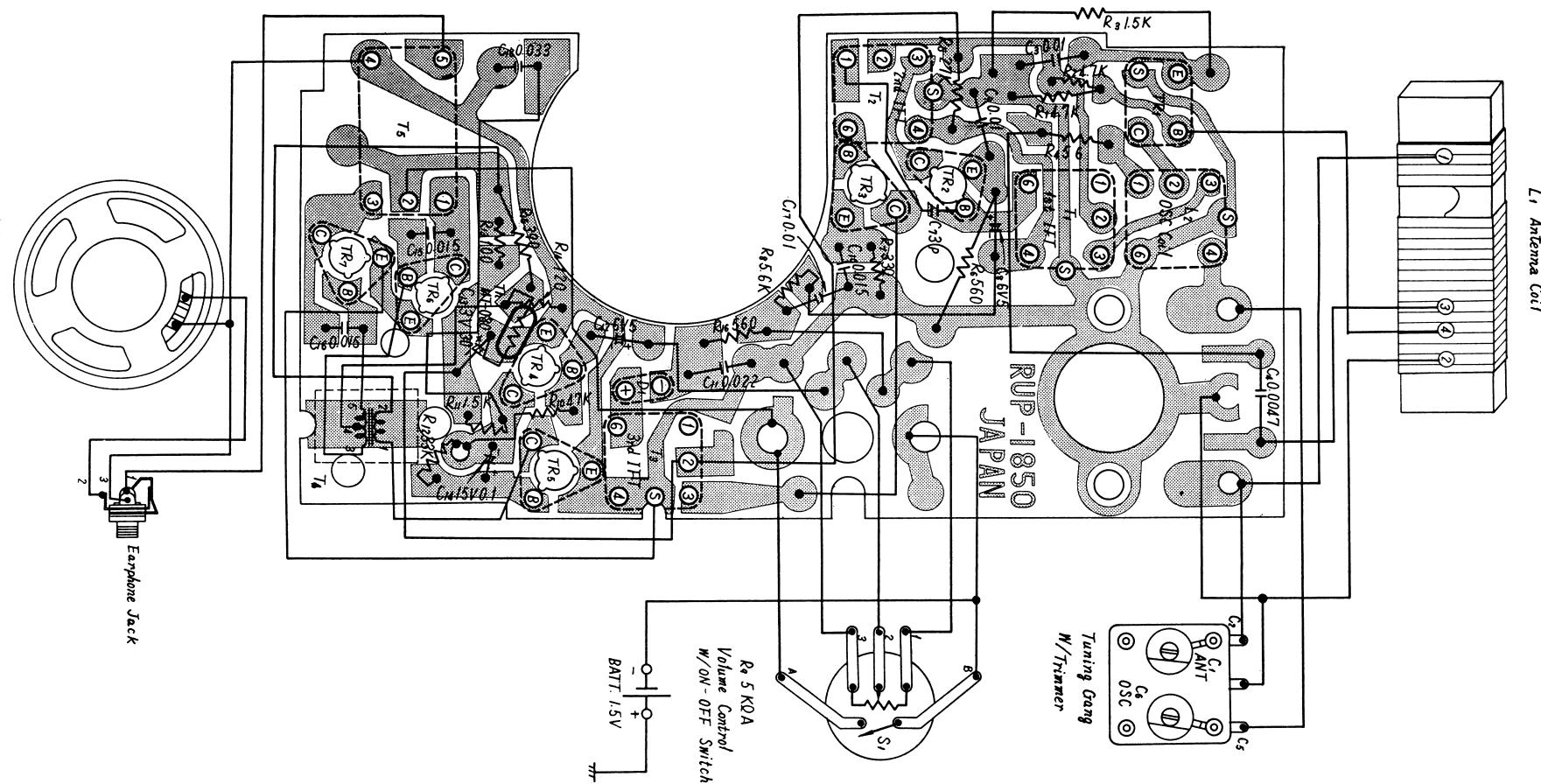
allowable deviations of resistors and capacitors as follows:

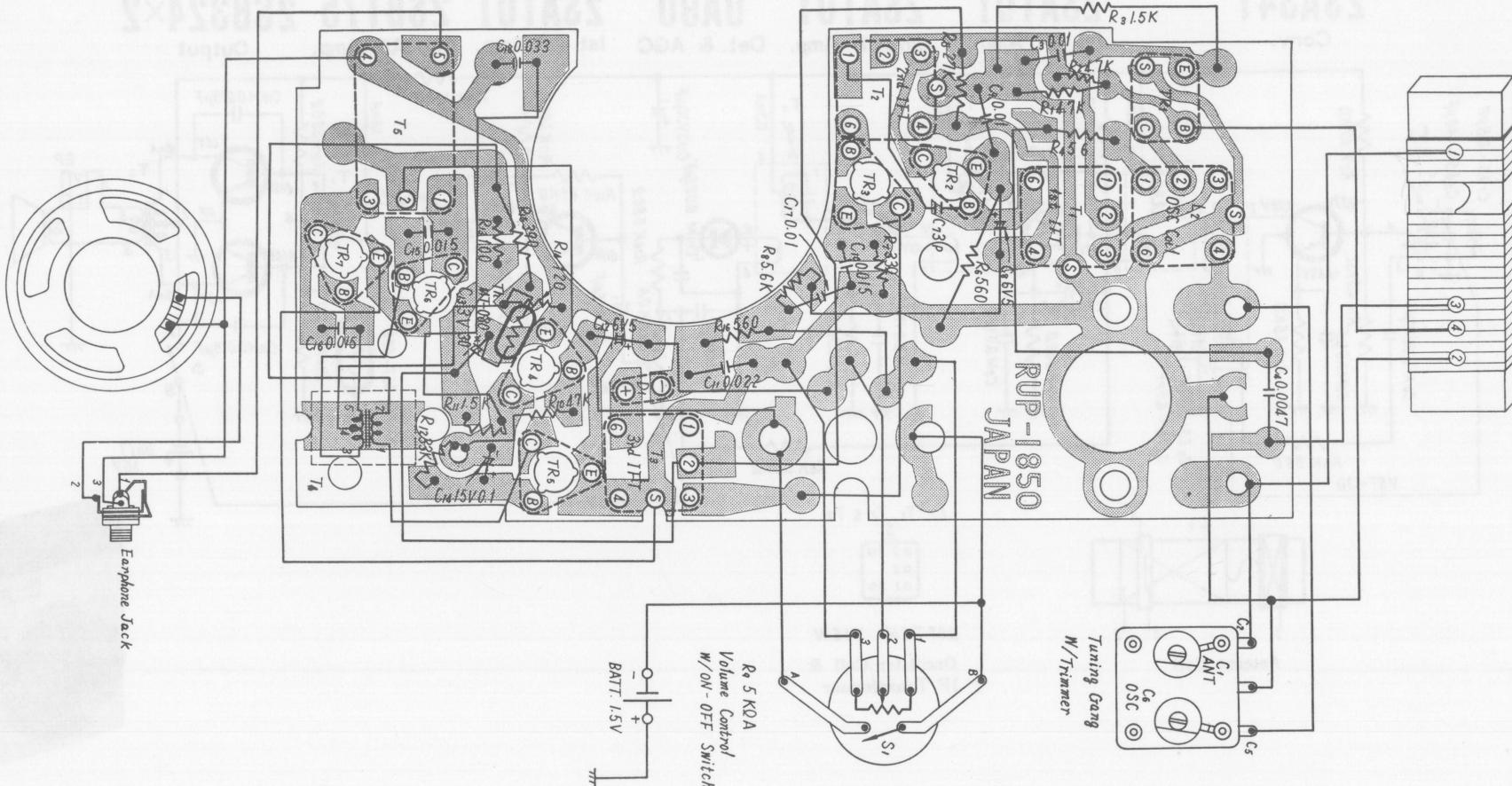
$$M = \pm 20\% \quad K = \pm 10\% \quad J = \pm 5\% \quad P = +100\% \\ - 0\%$$

5. PF = pico farad = mmf
6. Battery Current:

No signal..... 20mA  
 Maximum output ... 130mA







1 1/2" PH Dynamic Speaker

Earphone Jack