

TR<sub>1</sub>  
**2SA102**  
Conv.

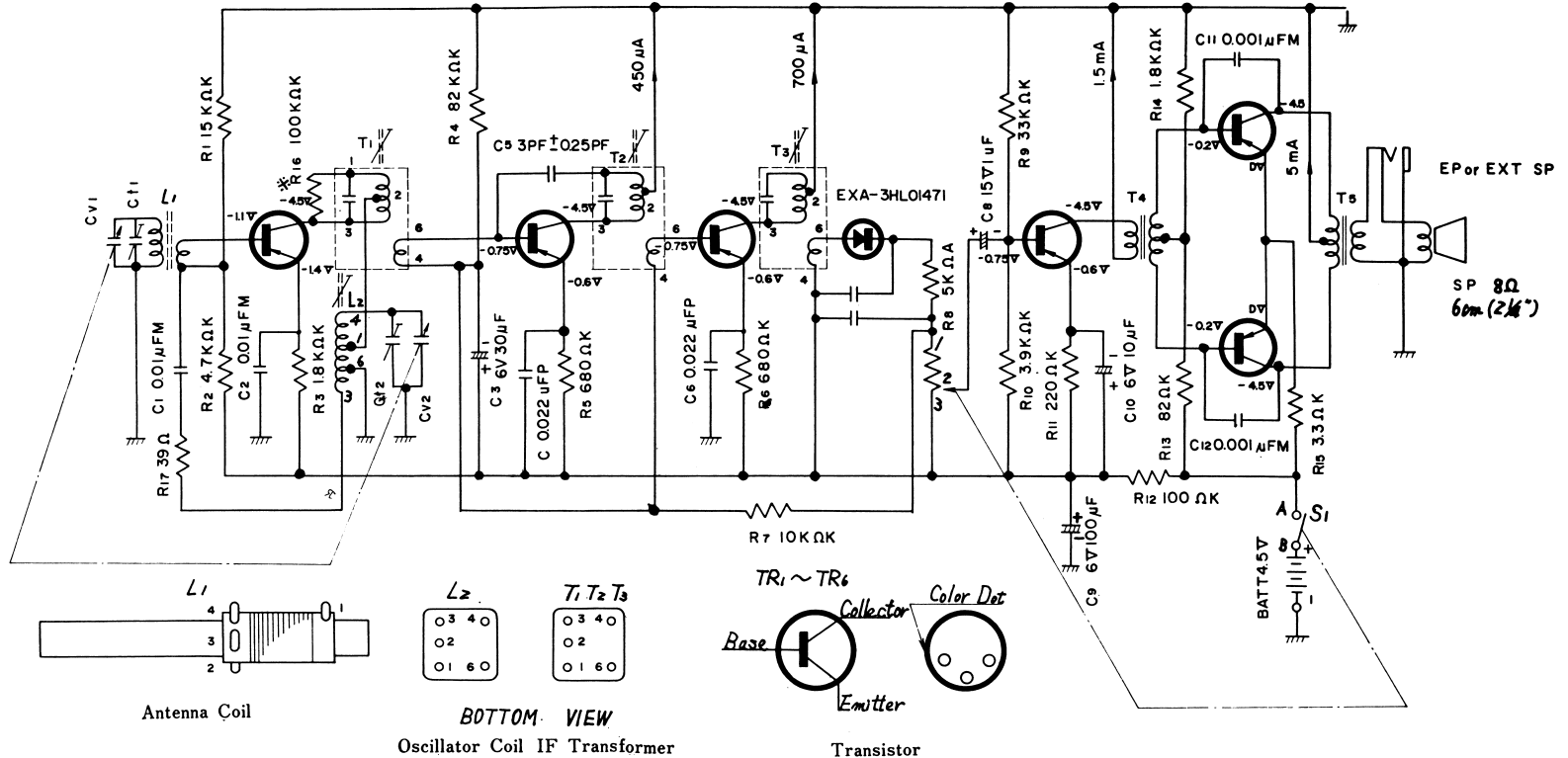
TR<sub>2</sub>  
**2SA101**  
1st IF Amp.

TR<sub>3</sub>  
**2SA101**  
2nd IF Amp.

D<sub>1</sub>  
**0A90**  
Det. & AGC

TR<sub>4</sub>  
**2SB175**  
AF Amp.

TR<sub>5</sub> & TR<sub>6</sub>  
**2SB176 × 2**  
Power Amp.



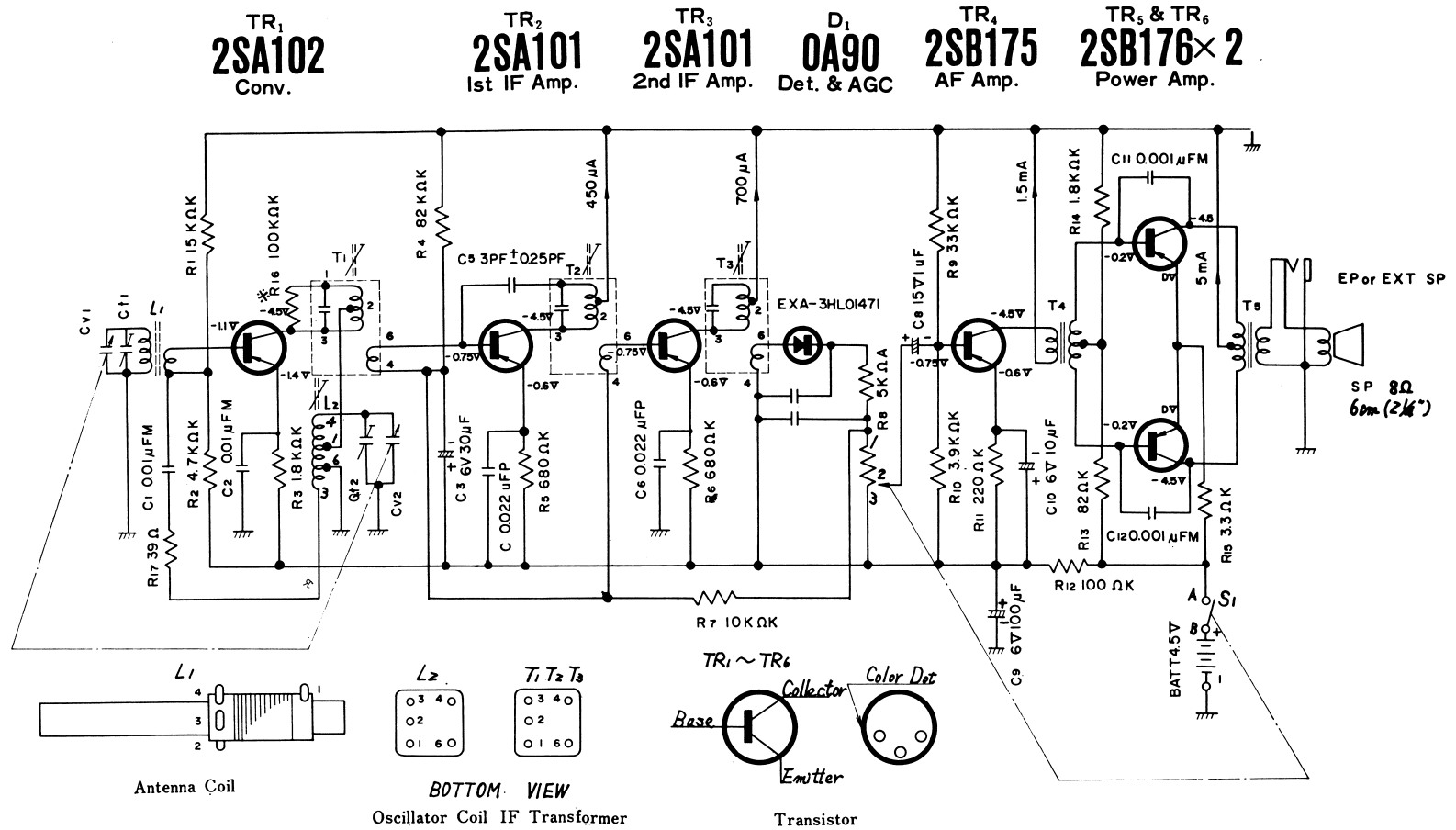
**Notes:**

1. S<sub>1</sub>: Power source switch in "OFF" position.
2. Voltage measurements are taken with circuit tester (10KΩ/V).
3. Measured voltages for TR<sub>1</sub>~TR<sub>6</sub> are from transistor terminal to bias line.
4. Capital letters (M, K, J, P) in the circuit diagram show allowable tolerances of resistors and capacitors as follows:  
M=±20% K=±10% J=±5% P=+100%  
- 0%

5. PF=Pico farad=mmf  
μF=micro farad=MF
6. The resistor dotted in the diagram are the standard value which may be variable according to the characteristics of transistor.  
\*R<sub>16</sub>=82KΩ or 220KΩ
7. Battery current: No signal.....10mA  
Maximum output.....65mA

Fig. 1 Schematic Diagram.

Fig. 1 Schematic Diagram.



**Notes:**

1. S<sub>1</sub>: Power source switch in "OFF" position.
2. Voltage measurements are taken with circuit tester (10KΩ/V).
3. Measured voltages for TR<sub>1</sub>~TR<sub>6</sub> are from transistor terminal to bias line.
4. Capital letters (M, K, J, P) in the circuit diagram show allowable tolerances of resistors and capacitors as follows:  
 M=±20% K=±10% J=±5% P=+100%  
 - 0%

5. PF=Pico farad=mmf  
 μF=micro farad=MF
6. The resistor dotted in the diagram are the standard value which may be variable according to the characteristics of transistor.  
 \*R<sub>16</sub>=82KΩ or 220KΩ
7. Battery current:  
 No signal.....10mA  
 Maximum output.....65mA